

ABSTRACT OF THE DISCLOSURE

In a flash memory including memory blocks and a controller for performing erasure and the like with respect to the memory blocks, in the 5 case where a deficient erasure status is output when a certain memory block receives an erasure command, a redundancy judging circuit stores an address at that time and an address inside of a redundant block as a redundant address with respect to the address at that time in a redundant address storage region. Thereafter, when the address is accessed, the 10 address is replaced with the redundant address stored in the redundant address storage region, so that a deficient memory block can be replaced with a redundant block.